



Isu

I hereby certify that this paper (along with any paper referred to as being attached and enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: May 4, 2006

Signature: 

(Andrew T. Zidel)

Docket No.: SCEI 3.0-178
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Koji Hirairi

Application No.: 10/801,308

Group Art Unit: 2195

Filed: March 16, 2004

Examiner: M. A. T. An

For: METHODS AND APPARATUS FOR
REDUCING POWER DISSIPATION IN A
MULTI-PROCESSOR SYSTEM

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

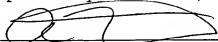
Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each non-US reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: May 4, 2006

Respectfully submitted,

By 
Andrew T. Zidel
Registration No.: 45,256
LERNER, DAVID, LITTENBERG,
KRUMHOLZ & MENTLIK, LLP
600 South Avenue West
Westfield, New Jersey 07090
(908) 654-5000
Attorney for Applicant

LD-458\



PTO/SB/08a/b (07-05)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

| | | | | | |
|--|---|----|---|--------------------------|------------------------|
| Substitute for form 1449A/B/PTO | | | | Complete if Known | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | | | Application Number | 10/801,308-Conf. #2000 |
| | | | | Filing Date | March 16, 2004 |
| | | | | First Named Inventor | Koji Hirairi |
| | | | | Art Unit | 2127 |
| | | | | Examiner Name | Not Yet Assigned TANG |
| Sheet | 1 | of | 1 | Attorney Docket Number | SCEI 3.0-178 |

| U.S. PATENT DOCUMENTS | | | | | |
|-----------------------|-----------------------|---|--------------------------------|--|---|
| Examiner Initials* | Cite No. ¹ | Document Number Number-Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| /K/T/ | AA* | US-2002/0053684-A1 | 05/2002 | Chauvel et al. | |
| | AB* | US-2002/0065049-A1 | 05/2002 | Chauvel et al. | |
| | AC* | US-2003/0125900-A1 | 07/2003 | Orenstien et al. | |
| | AD* | US-5,274,797 | 12/1993 | Barlow et al. | |
| | AE* | US-5,715,184 | 02/1998 | Tyler et al. | |
| /K/T/ | AF* | US-5,761,516 | 06/1998 | Rostoker et al. | |

| FOREIGN PATENT DOCUMENTS | | | | | |
|--------------------------|-----------------------|--|--------------------------------|--|---|
| Examiner Initials* | Cite No. ¹ | Foreign Patent Document Country Code ³ -Number-Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * CITE NO.: Those application(s) which are marked with a single asterisk (*) next to the Cite No. are not supplied (under 37 CFR 1.98(a)(2)(ii)) because that application was filed after June 30, 2003 or is available in the IFW. ¹ Applicant's unique citation designation number (optional). ² See Kind Code of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

| NON PATENT LITERATURE DOCUMENTS | | | | | |
|---------------------------------|-----------------------|--|--|--|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | T ² |
| /K/T/ | CA | Skadron et al., "Temperature-Aware Microarchitecture: Modeling and Implementation," ACM trans-on architecture and Code optimization, pp. 94-125, March 2004. | | | |
| | CB | Azevedo et al., "Architectural and Compiler Strategies for Dynamic Power Management in the COPPER Project," 2001 International Workshop on Innovative Architecture, January, 2001. | | | |
| | CC | Heo et al., "Reducing Power Density through Activity Migration," Proceedings of the 2003 International Symposium on Low Power Electronics and Design (ACM Press, ©2003), pp. 217-222. | | | |
| | CD | Tiwari et al., "Instruction Level Power Analysis and Optimization of Software," Journal of VLSI Signal Processing, Vol. 13, No.2 (August 1996), pp. 1-18. | | | |
| | CE | Abhishek Mehta, "Techniques for Low Power Processing - A Survey" CSE 822, Spring 2001, A29838087, Retrieved from the Internet on March 29, 2004, URL: w-w.cs.vcr.edu/~chzhnp/paners/read/lowpower_survey.pdf , 10 pages. | | | |
| | CF | Lee et al., "Power Analysis and Minimization Techniques for Embedded DSP Software," IEEE Transactions on VLSI Systems, Vol. 5, No. 1, March 1997, pp. 123-135 (manuscript copy submitted). | | | |
| /K/T/ | CG | Gerosa et al., "A 250-MHz 5-W PowerPC Microprocessor with On-Chip 1.2 Cache Controller," IEEE Journal of Solid-State Circuits, Vol. 32, No. 11, November 1997, pp. 1635-1649. | | | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

| | | | |
|--------------------|----------------|-----------------|------------|
| Examiner Signature | /Kenneth Tang/ | Date Considered | 01/31/2009 |
|--------------------|----------------|-----------------|------------|